IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Patent Number   7,873,813 B2

Issued: January 18, 2011

Name of Patentee: Jeroen Anton Johan Leijten

Title of Invention: Variable Length VLIW Instruction With Instruction Fetch Control Bits
                     For Prefetching, Stalling, Or Realigning In Order To Handle Padding
                     Bits And Instructions That Cross Memory Line Boundaries

REQUEST FOR CERTIFICATE OF CORRECTION OF
PATENT FOR PATENT OFFICE MISTAKE (37 CFR 1.322(a))

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Attn: Decision and Certificate of Correction
         Branch of the Patent Issue Division

Dear Sir:

It is requested that a Certificate of Correction be issued to correct an error found in the above-identified patent. Attached hereto is a Certificate of Correction which indicates the requested correction.

It is believed that there is no charge for this request since the applicant was not responsible for such error, as will be apparent upon a comparison of the issued patent with the application as allowed. However, the Commissioner is hereby authorized to charge any fee that may be required to Deposit Account No. 12-1216.

Respectfully submitted,

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Date: March 16, 2011

UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO.   :   7,873,813 B2

DATED   :   January 18, 2011

INVENTOR(S) :   Jeroen Anton Johan Leijten

It is certified that an error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Title page

Item [30] is missing and should read:

"(30)   Foreign Application Priority Data

Jan. 30, 2001   (EP)...................01200333.1"

MAILING ADDRESS OF SENDER:

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